



APPLICATION NOTE

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APPLICATION NOTE
Design & Installation of VersaLink PCB Connectors

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APPLICATION NOTE

Design & Installation of VersaLink Connectors

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REVISION HISTORY

REVISION	DATE	REVISED PAGES	REVISIONS
A	7/29/2020		Initial Release

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1.0 Purpose

This document describes the proper way to design and install VersaLink PCB connectors & contacts onto a PCB.

Deviating from the methods described herein will result in lesser performance.

2.0 Referenced Documents

Document Number/Name	Description
GVLM-BR/BS & Similar	VersaLink MicroD PCB Connectors
853-052/054, 853-065/067, 853-079/080 & Similar	VersaLink Bridge PCB Connectors

Table 1. Reference Documents

3.0 Responsibility

This document is the responsibility of the Engineering team.

4.0 Methods of Performance Optimization

4.1 PCB Layout & Design

To achieve optimal impedance matching at the PC tail/PCB transition, we recommend the follow layout:

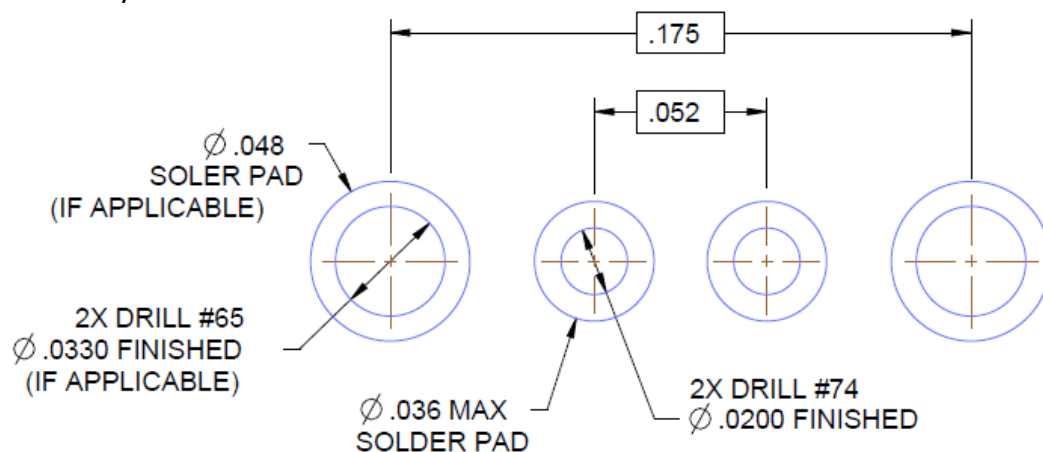


Figure 1 – Optimized PCB Layout

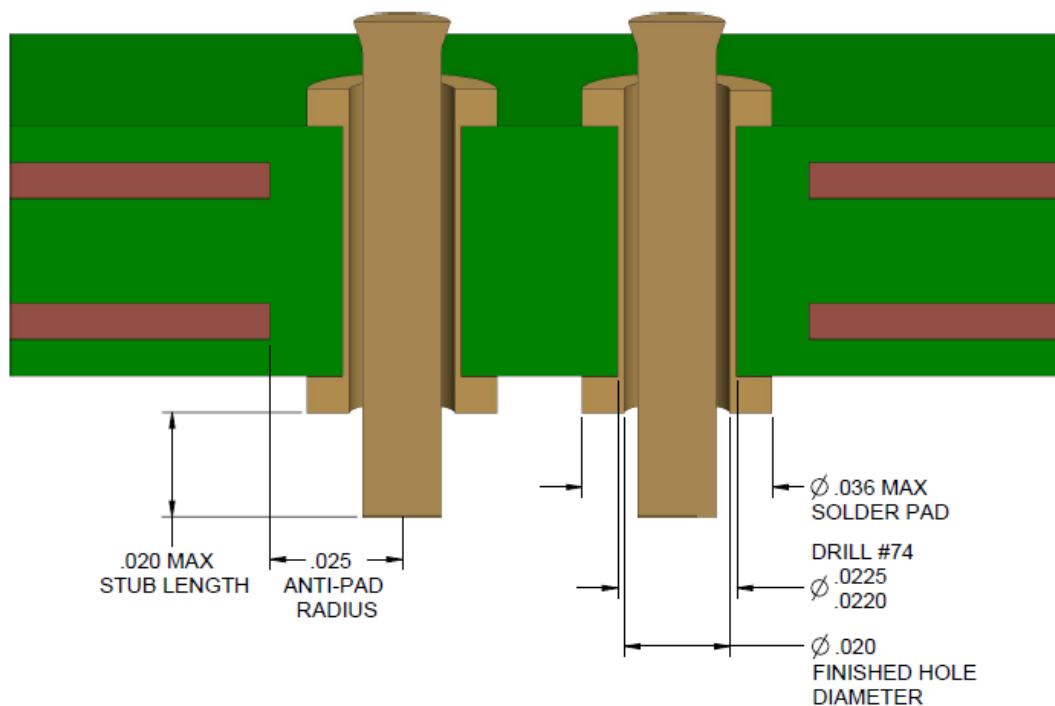


Figure 2 – Optimized PCB Cross Section

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4.2 Installation onto Board

Trim the PC tails so a stub length of .020 inches or less is obtained per Figure 2. This minimizes the length of capacitive stubs which lower impedance and cause the signal to resonate.

4.3 Trace Routing

Route the traces for the high-speed lines on the opposite side of the PCB from the connector to further reduce the length of capacitive stubs.

The signal trace width needed for the desired impedance is dependent on the PCB Stack-up, PCB material, and whether the traces are routed as differential pairs or single-ended lines.

It is recommended that the trace be tear drop shaped as it comes off of the solder pad.

4.4 Other Considerations

When being used in a connector that does not have PCB stand-offs, design for the PCB to be .020 inches from the base of the VersaLink PCB per Figure 2.

Figure 1 – The two ground holes on the .175 spacing only apply to the 853 VersaLink Bridge connectors. They should not be included for any other VersaLink Connector.